

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/834,660
 Priority Filing Date April 12, 2001
 Inventor Luan C. Tran
 Assignee Micron Technology, Inc.
 Priority Group Art Unit 2813
 Priority Examiner L. Schillinger
 Attorney's Docket No. MI22-1921
 TITLE: Semiconductor Processing Methods of Forming Transistors, Semiconductor
 Processing Methods of Forming Dynamic Random Access Memory Circuitry,
 and Related Integrated Circuitry

Assistant Commissioner for Patents
 Washington, D. C. 20231
 Attention: Official Draftsman

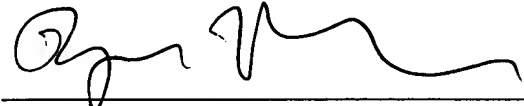
SUBSTITUTE DRAWING REQUEST

Enclosed are Red-line drawings of Figs. 3-7 along with substitute formal drawings, Figs. 1-7. Please enter the enclosed substitute formal drawings in the above-referenced application in place of drawings originally filed. The contents of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: Feb. 8, 2002

By: 
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Enclosures: 2 Red-line Drawings, Figs. 3-7 & 2 Sheets of Formal Drawings, Figs. 1-7.